

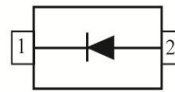
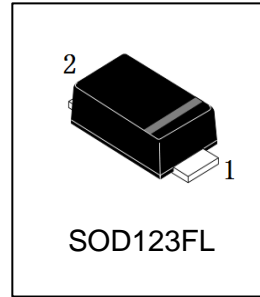
SODZ ***A -SH Series

GLASS PASSIVATED JUNCTION Zener voltage regulator diodes

1.0 Watt Steady State

Feature

- * 1 W SOD-123-FL
- * Zener voltage regulator diodes
- * Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- * We declare that the material of product compliance with RoHS requirements.
- * Guarding for over voltage protection
- * High temperature soldering guaranteed:
260°C/10 seconds at terminals
- * MSL: 1



Mechanical Data

Case: JEDEC SOD-123-FL/MINI SMA molded plastic

Terminals :Plated terminals, solderable per MIL-STD-750,Method 2026

Polarity: Color band denoted cathode except Bipolar

Mounting Position: Any

Weight : Approximated 0.0155 gram

We declare that the material of product is Haloggen free (green epoxy compound)

1.Electrical Characteristic

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

RATING	SYMBOL	VALUE	UNITS
Steady State Power Dissipation at $T_J=75^\circ\text{C}$ (Note1)	$P_{M(AV)}$	1.0	Watts
Z-current	I_Z	P_V/V_Z	mA
Operating and Storage Temperature Range	T_J, T_{STG}	-50 to +150	°C

NOTES:

1. 8.0mm² (.013mm thick) land areas
2. 8.3ms single half sine-wave, duty cycle= 4 pulses per minutes maximum.

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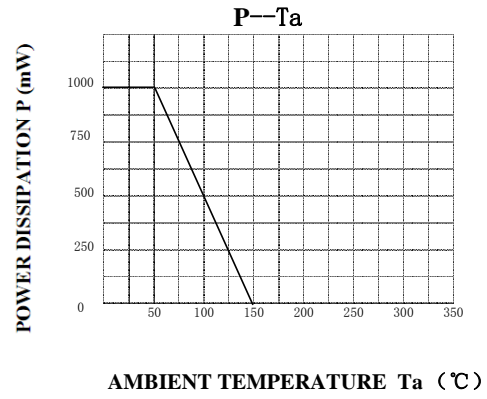
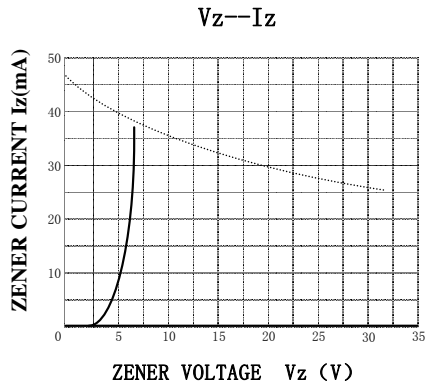
2.Product Characteristic

Vz tolerance : $\pm 5\%$; Tested with pulse $t_p=40\text{ms}$; $T_a=25^\circ\text{C}$ $V_{fmax}=1.2\text{V}$ @ $I_F=200\text{mA}$ $P=1\text{W}$

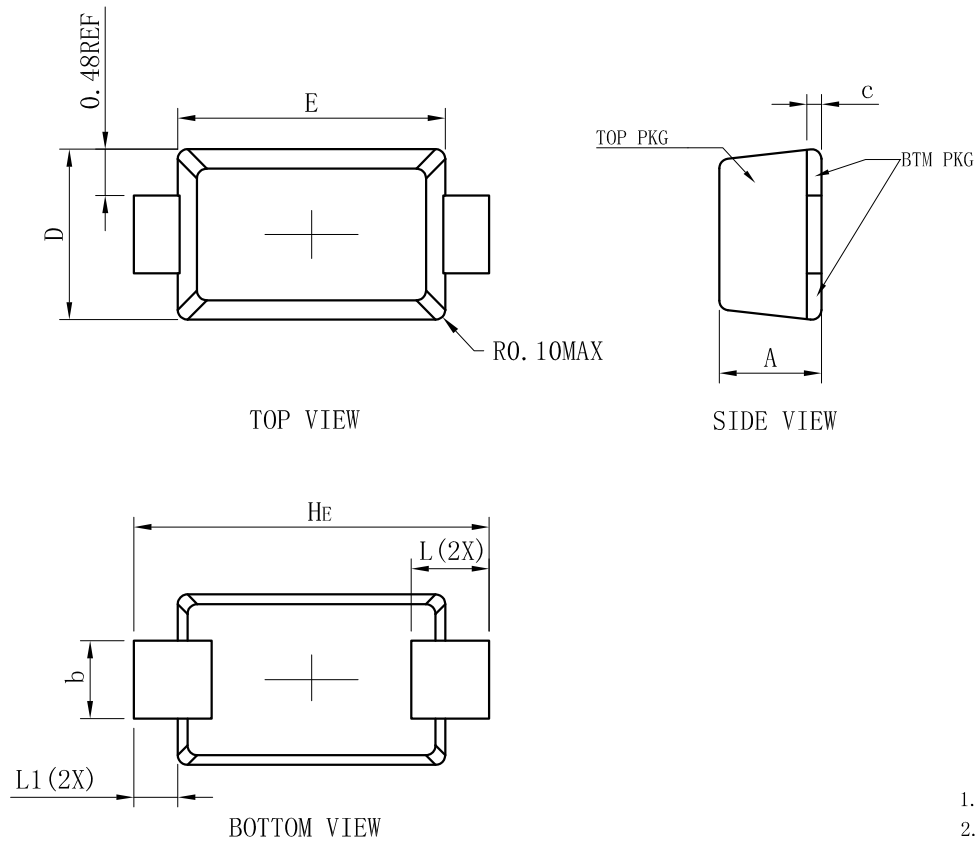
Type	Device marking code	Zener	Current	A and B Suffix only			Leakage Current		Maximum Regulator Current2)
		Voltage	I _{zt}	Z _{zt} @ I _{zt}	Z _{zk} @ I _{zk}	I _{zk}	I _R	V _R	I _{ZM} @ Tamb =50 ° C
		<u>V_z@I_{zt}</u>							
		Volts	mA	Ohms	Ohms	m A	uA Max	Volts	mA
SODZ2.4A-SH	Z2.4	2.4	5	95	500	1	600	1.0	178
SODZ2.7A-SH	Z2.7	2.7	5	95	500			1.0	164
SODZ3.0A-SH	Z3.0	3.0	5	95	500			1.0	146
SODZ3.3A-SH	Z3.3	3.3	5	95	500			1.0	133

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3.Characteristic Curves



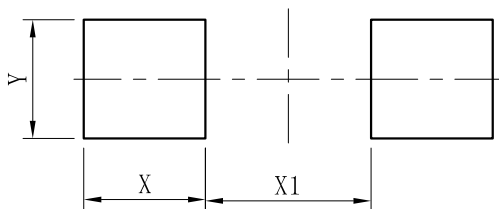
4. OUTLINE AND DIMENSIONS



GENERAL NOTES

1. Top package surface finish Ra0.4±0.2um
2. Bottom package surface finish Ra0.7±0.2um
3. Side package surface finish Ra0.4±0.2um

5. SOLDERING FOOTPRINT

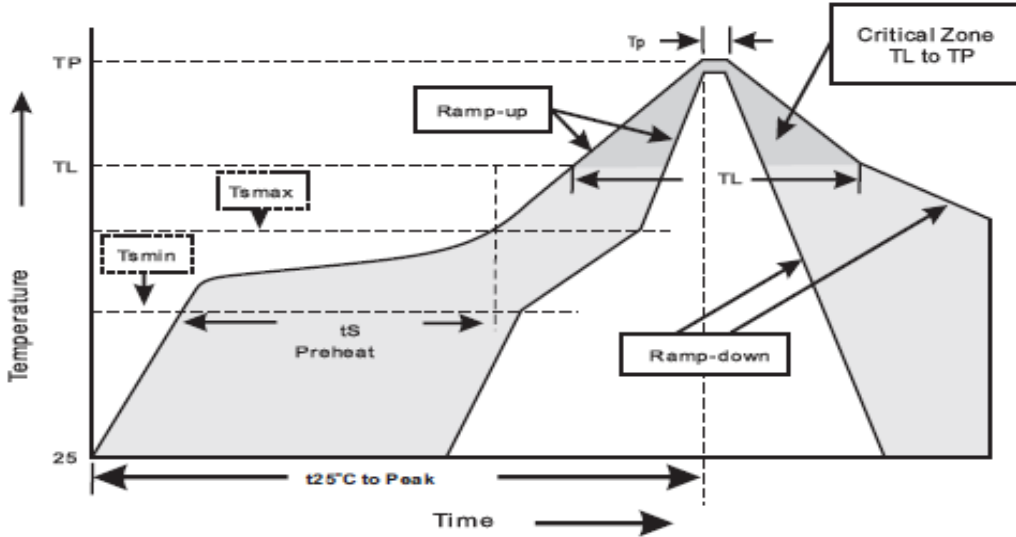


DIM	(mm)
X	1.20
Y	1.10
X1	2.00

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6. Suggested thermal profile for soldering process

1. Storage environment : Temperature=5~40°C Humidity=55±25%
2. Reflow soldering of surface-mount device



3. Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat	
- Temperature Min(T _{smin})	150°C
- Temperature Max(T _{smax})	200°C
- Time(min to max)(t _s)	60~120sec
T _{smax} to T _L	
- Ramp-up Rate	<3sec
Time maintained above:	
- Temperature (T _L)	217°C
- Time(t _L)	60-260sec
Peak Temperature(T _P)	255 -0/+5°C
Time within 5°C of actual Peak Temperature(T _P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

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7.High reliability test capabilities

Item Test	Condition	Reference
Solder Resistance	at 260±5°C for 10±2sec immerse body into solder 1/16" ± 1/32"	MIL-STD-750D METHOD-2031
Solderability	at 245±5°C for 5 sec	MIL-STD-202F METHOD-208
High Temperature Reverse Bias	V _R =80% rate at T _J =150°C for 168hrs	MIL-STD-750D METHOD-1038
Forward Operation Life	Rated average rectifier current T _A =25°C for 500hrs	MIL-STD-750D METHOD-1027
Intermittent Operation Life	T _A =25°C , I _F =I _o On state:power on for 5 min. Off state:power off for 5 min. on and off for 500 cycles	MIL-STD-750D METHOD-1036
Pressure Cooker	15P _{SIG} at T _A =121°C for 4hrs	JESD22-A102
Temperature Cycling	-55°C to +125°C dwelled for 30 min. and transferred for 5min. Total 10 cycles	MIL-STD-750D METHOD-1051
Thermal Shock	0°C for 5min. Rise to 100°C for 5min. Total 10 cycles	MIL-STD-750D METHOD-1056
Forward Surge	8.3ms single half sine-wave superimposed on rated load,one surge	MIL-STD-750D METHOD-4066-2
Humidity	at T _A =85°C , RH=85% for 1000hrs	MIL-STD-750D METHOD-1021
High Temperature Storage Life	at 175°C for 1000hrs	MIL-STD-750D METHOD-1031

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Proprietary Information

Title: Power Packages Product Packing Specification

功率产品包装规范

Document Number: APS-QA-QS-009

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8.1.2 Label position and QA stamp position.(Empty area) 标签张贴位置及QA印章位置。(印章盖在标签空白区)



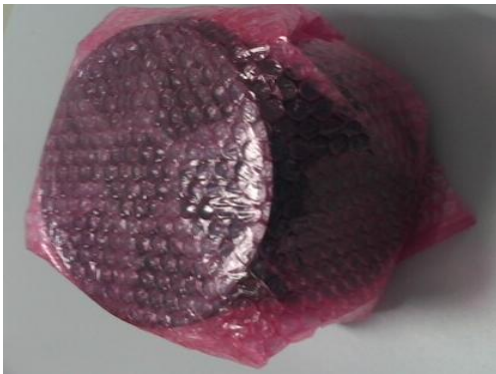
7英寸卷盘标签张贴及QA印章位置



13英寸卷盘标签张贴及QA印章位置

8.1.3 Ensure direction In the same reel. The same steel coil plate direction, With antistatic bubble to package reel. Refer to the below picture.

同一箱内的卷盘方向一致,用防静电泡沫对卷盘进行包裹。



7英寸卷盘防静电泡沫包裹



13英寸卷盘防静电泡沫包裹

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8.1.4 Put in the antistatic packing box after packaged reels. And QA stamp on the box label .

将包装好的卷盘放入防静电纸箱中，并在盒标签上盖章。



7 英寸卷盘内盒及标签



13 英寸卷盘内盒及标签

8.1.5 Product use printing inner box. 产品使用LRC印字内箱。



7英寸卷盘内箱印字（侧面）



13英寸卷盘内箱印字（正面）

8.1.6 Inner box packing quantity requirement. 内盒包装数量要求。

Product Description	QTY
SOD123-FL	1-10Reels
SOD323-HE	1-10Reels
SMA-FL	1-7Reels
SMB-FL	1-4Reels

8.1.7 With transparent tape sealing. 透明胶带封箱。



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7英寸内箱封盒



13英寸内箱封盒

8.1.8 Outer box size and packing quantity requirement, 外箱尺寸及包装数量要求。

Product Description	卷盘尺寸	Height (H)	Width (W)	Length (L)	Max. Qty
Power Device	7 英寸	410mm	400mm	445mm	12
Power Device	13 英寸	410mm	400mm	445mm	5



7 英寸卷盘产品装箱



13 英寸卷盘产品装箱

统一方向



Proprietary Information

Title: Power Packages Marking & Taping Specification

功率封装字模和编带规范

Document Number: APS-QA-QS-010

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8.2 Standard Products Taping Specification

标准产品编带规范

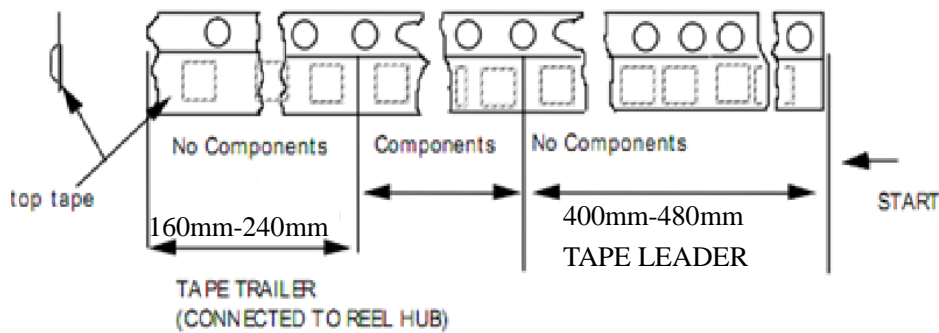
8.2.1 Tape length of no component

空带长度说明

Taping leader length 引导部分: 440mm±40mm , Tape trailer 尾部: 200mm±40mm

Figure 4

Tape Ends For Finished Goods Reel



8.2.2 Component packaging orientation: The cathode lead is close to the carrier tape's index hole.

产品放置方向: 印阴极带引脚邻近载带索引孔





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8.2.3 Tape enwind orientation

编带缠绕方向要求

