

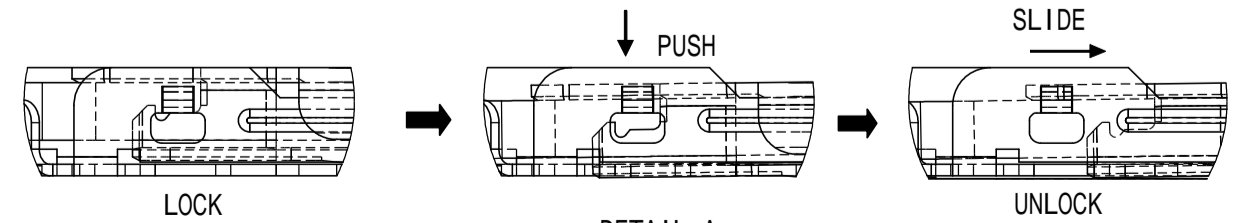
- RESTRICTED AREA(No pattern and Via hole)
- Limited wiring area (Only same line for each contact is allowed.)
- RESIST AREA(Connector metallic bottom area)

All the pattern and Via holes under the connector should be coated by resist.

APPLICABLE PWB. DIMENSION(REF.)

NOTE1. LOT INFORMATION IS SHOWN AS BELOW.
e.g. **23051**
PRODUCTION LINE NO.
PRODUCTION DATE (YMDD)

PART No.	PART NAME	BASE PLATING	TOP PLATING		
			CONTACT AREA	TERMINAL AREA	Lock / Hinge area
1	CONTACT	Ni (2~6μm)	Au (0.5μm min.)	Au (0.03μm min.)	-
4	GROUND-FRAME	Ni (2~6μm)	-	Au (0.03μm min.)	Au (0.03μm min.)



4	GROUND-FRAME	1	COPPER ALLOY	TABLE 1	
3	COVER	1	STAINLESS	-	
2	HOUSING	1	GLASS FILLED LCP	-	
1	CONTACT	6	COPPER ALLOY	TABLE 1	
符号 NO.	名称 DESCRIPTION	個数 QTY.	材料 MATERIAL	仕上 FINISH	備考 REMARKS
仕様書 (SPECIFICATION)		第1版 (ORIGINAL DATE)		シリーズ (SERIES)	
JACS-10922		05/MAR/2012		SF7	
一般公差 (GENERAL TOLERANCE)		製図 DR.		名称 (TITLE)	
寸法 (DIMENSION)		担当 CHK.		SF7W006S4H	
角度 (ANGLES)		査閲 APPD.		M. SHIMADA	
承認 APPD.		承認 APPD.		A. NATORI	
		質量 (MASS)		図面番号 (DRAWING NO.)	
				SJ112821	
				版数 (REV.)	
				1	