

VOLTAGE-CONTROLLED CRYSTAL OSCILLATOR (VCXO)

OUTPUT: LV-PECL, LVDS





Product Number VG3225EFN X1G005361xxxx00 VG5032EFN X1G005471xxxx00 VG7050EFN X1G005491xxxx00 VG3225VFN X1G005461xxxx00 VG5032VFN X1G005481xxxx00 VG7050VFN X1G005501xxxx00

VG3225 / 5032 / 7050EFN VG3225 / 5032 / 7050VFN

25 MHz to 250 MHz •Frequency range

 Supply voltage 3.3 V Typ. LV-PECL or LVDS Output Function Output enable (OE)

 $\pm 10 \times 10^{-6}$ Min. $/\pm 20 \times 10^{-6}$ Min. $/\pm 50 \times 10^{-6}$ Min Absolute pull range

•Operating temperature: -40 °C to +85 °C /-40 °C to +105 °C



VG3225VFN $(3.2 \times 2.5 \times 1.05 \text{ mm})$



VG5032VFN $(5.0 \times 3.2 \times 1.3 \text{ mm})$



VG7050VFN $(7.0 \times 5.0 \times 1.5 \text{ mm})$

Specifications (characteristics)

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	Symbol	Specifications				
lt		LV-PECL	LVDS	One ditions / Demonto		
Item		VG3225EFN / VG5032EFN /	VG3225VFN / VG5032VFN /	Conditions / Remarks		
		VG7050EFN	VG7050VFN			
Output frequency range	fo	25 MHz to	250 MHz	Please contact us for available frequencies.		
Supply voltage	Vcc	C: 3.3 V ± 0.165 V				
Control voltage	Vc	1.65 V ±1.65 V				
Storage temperature	T_stg	-55 °C to +125 °C				
Operating temperature	T_use	G: -40 °C to +85 °C, H: -40 °C to +105 °C				
Frequency tolerance	f_tol	J: ±50 x 10 ⁻⁶ Max.		Includes initial frequency tolerance voltage change and 10 years aging		
	APR	B: ±50 × 10 ⁻⁶ Min.		25 MHz ≤ fo ≤ 42.5 MHz, 50 MHz ≤ fo ≤ 85 MHz, 100 MHz ≤ fo ≤ 170 MHz		
Absolute Pull range *1				Full frequency range		
_		S: ±10 × 10 ⁻⁶ Min.		Full frequency range		
Current consumption	Icc	60 mA Max.	25 mA Max.	OE = V_{CC} , L_ECL = 50 Ω or L_LVDS = 100 Ω		
Disable current	I_dis	25 mA Max.	15 mA Max.	OE = GND		
Input impedance	Zin	10 MΩ	Ω Min.	DC level		
Frequency change polarity	-	Positive slope		$V_C = 0 \text{ V to } 3.3 \text{ V}$		
Symmetry	SYM	45 % to 55 %		At output crossing point		
Output voltage (LV-PECL)	V _{OH}	V _{CC} - 1.1 V Min. V _{CC} - 1.5 V Max.		DC characteristics		
0	Von		250 mV to 450 mV	Differential output voltage, V _{OD1} , V	OD2	50 1
Output voltage (LVDS)	Vos	_	1.15 V to 1.35 V	Offset voltage, V _{OS1} , V _{OS2}	002	DC characteristics
ECL load condition	L_ECL	50 Ω	_	Terminated to Vcc - 2.0 V		
LVDS load condition	L_LVDS	-	100 Ω	Connected between OUT to OUT		
Input voltage	V _{IH}	70 % V _{CC} Min. 30 % V _{CC} Max.		OE terminal		
	VIL					
Rise/Fall times	tr / tf	0.5 ns Max	0.3 ns Max.	LV-PECL: Between 20 % and 80 % of (V _{OH} - V _{OL}) LVDS: Between 20 % and 80 % of Differential Output peak to peak voltage		
Startup time	t_str	10 ms	Max.	Time at minimum supply voltage to	Time at minimum supply voltage to be 0 s	
Phase Jitter	t _{PJ}	120 fs Max.	160 fs Max.	fo = 122.88 MHz	Offset Frequency 12 kHz to 20 MHz	
		80 fs Max.	80 fs Max.	fo = 245.76 MHz	Onset i requerity 12 kmz to 20 Mmz	

^{*1} Absolute pull range = Frequency control range- Frequency tolerance

Product Name (Standard form)

<u>VG3225 EFN 122.880000MHz C J G H B A</u> 1 3 456789

①Model ②Output (E: LV-PECL, V: LVDS) ③Frequency

④Supply voltage (C: 3.3 V Typ.) ⑤Frequency tolerance ⑥Operating temperature

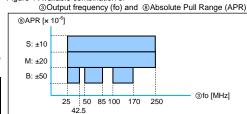
⑤Frequency tolerance ±50 × 10

		•
	6	perating temperature
	G	-40 to +85 °C
	Н	-40 to ±105 °C

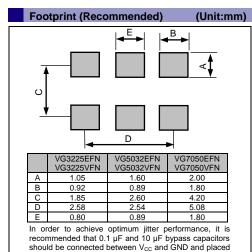
⑦0	OE Function	
Н	Active High	

,	.) [-	- ()					
	®Α						
	В	±50 × 10 ⁻⁶					
	М	±20 × 10 ⁻⁶					
	9	±10 ∨ 10 ⁻⁶					

Figure 1 Available combination of



External dimensions (Unit:mm) VG7050EFN VG5032EFN VG3225EFN VG7050VFN VG5032VFN VG3225VFN Pin map Pin Connection OE GND 3 4 OUT OE pin = HIGH or "Open": Specified frequency output. 5 OUT OE pin = LOW: Output is high impedance 6



as close to the V_{CC} pin as possible.

 $^{^{\}star}$ Please keep Vc pin open or ground while powering up Vcc.

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At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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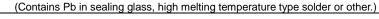
►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.





▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



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