

### **CRYSTAL OSCILLATOR (SPXO)**

**OUTPUT: LV-PECL, LVDS** 



Product Number SG2520EGN: X1G005881xxxx15 SG2520VGN: X1G005901xxxx15

# SG2520EGN SG2520VGN

•Frequency range : 25 MHz to 500 MHz

•Supply voltage : 1.8 V Typ. (LVDS only) / 2.5 V Typ. / 3.3 V Typ.

•Frequency tolerance :  $\pm 25 \times 10^{-6}$ ,  $\pm 30 \times 10^{-6}$ ,  $\pm 50 \times 10^{-6}$ •Operating temperature : -40 °C to +85 °C, -40 °C to +105 °C •Function : Output enable (OE) or Standby ( $\overline{ST}$ ) •Phase jitter : 50 fs Max. (fo = 491.52 MHz)



 $(2.5 \times 2.0 \times 0.74 \text{ mm})$ 

### Specifications (characteristics)

		Specifications					
Item	Symbol	LV-PECL	LVDS		Conditions / Remarks		
		SG2520EGN	SG2520\	/GN			
Output frequency range	fo		25 MHz to 500 MHz		Please contact us for avail	able frequencies.	
Supply voltage	Vcc	C: 3.3 V ± 5 % D: 2.5 V ± 5 %					
Storage temperature	T_stg		-55 °C to +125 °C	1			
Operating temperature	T use	G:	-40 °C to +85 °C, H: -40 °C to	+105 °C			
		D: ±25 × 10 <sup>-6</sup> Max.		Includes initial frequency to	olerance, frequency /		
Frequency tolerance	f_tol	E: ±30 × 10 <sup>-6</sup> Max.			temperature characteristics		
' '	_	J: ±50 × 10 <sup>-6</sup> Max.			coefficient and 10 years ag	ging (+25 °C)	
		60 mA Max. –		OE or $\overline{ST} = V_{CC}$ , L_ECL =	50 Ω		
Current consumption			25 mA / 30 mA / 25 mA Max.	25 mA / - / 25 mA Max.	25 MHz ≤ fo < 212 MHz	OE or $\overline{ST} = V_{CC}$ ,	
Current consumption	Icc	_	28 mA / 35 mA / 28 mA Max.	_	212 MHz ≤ fo < 392 MHz		
			28 mA / 35 mA / 30 mA Max.	_	392 MHz ≤ fo ≤ 500 MHz	Output option: A / B / C	
Disable current	I_dis	35 mA Max.	20 mA M	lax.	OE = GND		
Stand-by current	l otal		30 μA Max.		$\overline{ST}$ = GND, T_use Max. = -		
Stand-by current	I_std		60 μA Max.		$\overline{ST}$ = GND, T_use Max. = -	ST = GND, T_use Max. = +105 °C	
Symmetry	SYM	45 % to 55 %		At output crossing point			
Output voltage (LV-PECL)	V <sub>OH</sub> V <sub>OL</sub>	V <sub>CC</sub> - 1.1 V Min. V <sub>CC</sub> - 1.5 V Max.		Output option: A, DC chara	acteristic		
	V <sub>SW</sub>	0.8 V to 2.0 V	500 mV to 900 mV	500 mV to 900 mV	Output option: A		
Differential swing		_	800 mV to 1 600 mV	_	Output option: B		
		_	600 mV to 1 200 mV	600 mV to 1 200 mV	Output option: C		
		-	250 mV to 450 mV	250 mV to 450 mV	Output option: A	Differential output valters	
	Vod		400 mV to 800 mV	_	Output option: B	Differential output voltage, V <sub>OD1</sub> , V <sub>OD2</sub>	
Output voltage (LVDS)			300 mV to 600 mV	300 mV to 600 mV	Output option: C		
Output voltage (LVDS)	$dV_{OD}$	-	50 mV Max.		$dV_{OD} =  V_{OD1} - V_{OD2} $		
	Vos	_	1.15 V to 1.35 V	0.65 V to 0.85 V	Offset voltage, Vos1, Vos2		
	dVos	-	50 mV Max.		$dV_{OS} =  V_{OS1} - V_{OS2} $		
Output load condition	L_ECL	50 Ω	_		Terminated to V <sub>CC</sub> - 2.0 V		
Output load condition	L_LVDS	– 100 Ω		Connected between OUT a	and OUT		
Input voltage	V <sub>IH</sub>	70 % V <sub>CC</sub> Min.		OE or ST terminal			
input voitage	VIL	30 % V <sub>CC</sub> Max.					
Rise/Fall times	tr/tf	0.35 ns Max.		LV-PECL: 20 % - 80 % (V LVDS: 20 % - 80 % dif	он - V <sub>OL</sub> ) fferential output peak to peak		
Start-up time	t str	10 ms Max.		t = 0 at 90 % Vcc			
Phase jitter	tpJ	250 fs Max.	250 fs Max.	400 fs Max.	25 MHz ≤ fo < 100 MHz	Offset frequency	
		90 fs Max.	100 fs Max.	130 fs Max.	100 MHz ≤ fo ≤ 156 MHz	fo < 50 MHz:	
		70 fs Max.	60 fs Max.	70 fs Max.	156 MHz < fo ≤ 212 MHz	12 kHz to 5 MHz	
		60 fs Max.	50 fs Max.	60 fs Max.	212 MHz < fo ≤ 391 MHz	fo ≥ 50 MHz:	
		50 fs Max.	50 fs Max.	60 fs Max.	391 MHz < fo ≤ 500 MHz	12 kHz to 20 MHz	

Product Name (Standard form)

### <u>SG2520 EGN 156.250000MHz C D H P Z A</u>

n) ① ② ③ ④⑤⑥⑦

①Model ②Output (E: LV-PECL, V: LVDS) ③Frequency ④Supply voltage ⑤Frequency tolerance ⑥Operating temperature ⑦Function ⑥Output disable type (Z: High impedance) ⑨Output option

	-1 3 - 1	
Supply voltage		
С	3.3 V Typ.	
D	2.5 V Typ.	
E*	1.8 V Typ.	

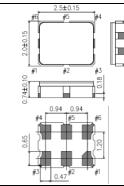
-			
(5)	⑤Freq. tolerance		
D	$\pm 25 \times 10^{-6}$		
Е	$\pm 30 \times 10^{-6}$		
J	±50 × 10 <sup>-6</sup>		

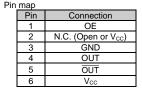
1 .	
6	Operating temp.
G	-40 °C to +85 °C
Н	-40 °C to +105 °C

⑦Function		
Р	OE	
S	S₹	

Output option		
	SG2520EGN	SG2520VGN
Α	Default	V <sub>SW</sub> = 500 mV to 900 mV
В	-	$V_{SW} = 800 \text{ mV} \text{ to } 1600 \text{ mV}$
ر	_	$V_{ow} = 600 \text{ m} \text{ / to } 1.200 \text{ m} \text{ / }$

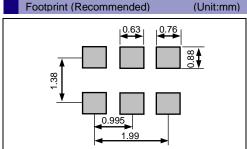
## External dimensions (Unit:mm)





Note:

OE pin = HIGH or "Open": Specified frequency output.
OE pin = LOW: Output is high impedance



In order to achieve optimum jitter performance, it is recommended that 0.1  $\mu F$  and 10  $\mu F$  bypass capacitors should be connected between  $V_{CC}$  and GND and placed as close to the  $V_{CC}$  pin as possible.

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All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

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Explanation of the mark that are using it for the catalog



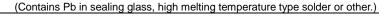
►Pb free.



► Complies with EU RoHS directive.

\*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.





▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



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