Chip Resistor Surface Mount AH SERIES 0603/0805/1206 (RoHS Compliant)

8

<u>SCOPE</u>

This specification describes AH0603/0805/1206 high voltage and anti-sulfurated chip resistors with lead-free terminations made by thick film process.

APPLICATIONS

- Converter
- Printer equipment
- Battery charger
- Computer
- Power supply
- Car electronics

FEATURES

- AEC-Q200 qualified
- RoHS compliant
- Reducing environmentally hazardous wastes
- High component and equipment reliability
- Non-forbidden materials used in products/production
- Halogen Free Epoxy
- Moisture sensitivity level: MSL 1
- IEC 62368-1:2014 safety certificate (G.10.2) issued by UL Demko for the following sizes and resistance ranges:
 - 0603: $100 K\Omega$ to $10 M\Omega$
 - 0805: 100KΩ to 22MΩ
 - I206: I00KΩ to 27MΩ
 *Please refer to UL certification

ORDERING INFORMATION - GLOBAL PART NUMBER & 12NC

Both part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

YAGEO BRAND ordering code GLOBAL PART NUMBER (PREFERRED)

AH <u>XXXX X X X XX XXXX L</u>

(1) (2) (3) (4) (5) (6) (7)

(I) SIZE

0603/0805/1206

(2) TOLERANCE

- D = ±0.5%
- $F = \pm 1\%$
- $J = \pm 5\%$

(3) PACKAGING TYPE

- R = Paper/PE taping reel
- K = Embossed taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

(5) TAPING REEL

07= 7 inch dia. Reel

(6) RESISTANCE VALUE

There are $2\sim4$ digits indicated the resistor value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g. IK2, not IK20.

Detailed resistance rules show in table of "Resistance rule of global part number".

(7) DEFAULT CODE

Letter L is system default code for ordering only $^{\left(\text{Note}\right) }$

Resistance rule of global part

number Resistance code r	ule Example
XXKX	0K = 0,000 Ω
(10 to 97.6 KΩ)	97K6 = 97,600 Ω
XXXK	$100K = 100,000\Omega$
(100 to 976 KΩ)	976K = 976,000Ω
XMXX	$ M = ,000,000 \Omega$
(Ι to 9.76 MΩ)	$9M76 = 9,760,000 \ \Omega$
XXMX	$10M = 10,000,000 \Omega$
(10 to 16 MΩ)	$27M = 27,000,000 \Omega$

ORDERING EXAMPLE

The ordering code of a AH1206 chip resistor, value 1 M Ω with ±5% tolerance, supplied in 7-inch tape reel is: AH1206JR-071ML.

NOTE

- All our R-Chip products meet RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART NUMBER / I2NC can be added (both are on customer request)

AGEO Phicom	
Chip Ke	Sistor Surface Mount AH SERIES 0603/0805/1206 (RoHS Compliant)
IARKING	
H0603/0805/1206	
103	E-24 series: 3 digits, ±5%
Fig. I Value=10 KΩ	First two digits for significant figure and 3rd digit for number of zeros
AH0603	
	Γ 24 contrary 2 distant +0 Γ 9/ 9 + 19/
<u> </u>	E-24 series: 3 digits, ±0.5% & ±1% Exception values 10/11/13/15/20/75 of E24 series
Fig. 2 Value= 24Ω	One short bar under marking letter
	E-96 series: 3 digits, ±0.5% & ±1%
	Including values 10/11/13/15/20/75 of E24 series
Fig. 3 Value=12.4 KΩ	First two digits for E-96 marking rule and 3rd letter for number of zeros
AH0805/1206	
1002	Both E-24 and E-96 series: 4 digits, $\pm 0.5\% \& \pm 1\%$



Both E-24 and E-96 series: 4 digits, $\pm 0.5\% \& \pm 1\%$ First three digits for significant figure and 4th digit for number of zeros

For further marking information, please refer to data sheet "Chip resistors marking".

CONSTRUCTION

The resistor is constructed on top of a high-grade ceramic body. Internal metal electrodes are added on each end to make the contacts to the thick film resistive element. The composition of the resistive element is a noble metal imbedded into a glass and covered by a second glass to prevent environment influences. The resistor is laser trimmed to the rated resistance value. The resistor is covered with a protective epoxy coat, finally the two external terminations (matte tin on Nibarrier) are added. See fig.5

DIMENSIONS

Table I For outlines see fig. 5

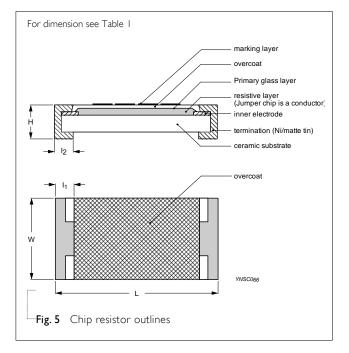
 TYPE
 L (mm)
 W (mm)
 H (mm)
 I1 (mm)
 I2 (mm)

 AH0603
 1.60 ±0.10
 0.80 ±0.10
 0.45 ±0.10
 0.25 ±0.15
 0.25 ±0.15

 AH0805
 2.00 ±0.10
 1.25 ±0.10
 0.50 ±0.10
 0.35 ±0.20
 0.35 ±0.20

 AH1206
 3.10 ±0.10
 1.60 ±0.10
 0.55 ±0.10
 0.45 ±0.20
 0.40 ±0.20

OUTLINES



Chip Resistor Surface Mount AH SERIES 0603/0805/1206 (RoHS Compliant)

ELECTRICAL CHARACTERISTICS

Table 2	2						
		CHARACTERISTICS					
ТҮРЕ	RESISTANCE RANGE	Rated Power	Operating Temperature Range	Max. Working Voltage	Max. Overload Voltage	Dielectric Withstanding Voltage	Temperature Coefficient of Resistance
AH0603	5% (E-24) 47Ω to 10MΩ 1% (E-24/E-96) 47Ω to 10MΩ 0.5% (E-24/E-96) 47Ω to 10MΩ	1/10W		350V	500V	500V	
AH0805	5% (E-24) 47Ω to 22MΩ 1% (E-24/E-96) 47Ω to 22MΩ 0.5% (E-24/E-96) 47Ω to 10MΩ	1/8 W	–55 ℃ to +155 ℃	400 V	800 V	800 ∨	±200 ppm/°C
AH1206	5% (E-24) 47Ω to 27MΩ 1% (E-24/E-96) 47Ω to 27MΩ 0.5% (E-24/E-96) 47Ω to 15MΩ	1/4 W		500 V	1,000 ∨	1,000 ∨	

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please refer to data sheet "Chip resistors mounting".

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity					
PACKING STYLE	REEL DIMENSION	AH 0603	AH 0805	AH 1206	
Paper/PE taping reel (R)	7" (178 mm)	5,000	5,000	5,000	
Embossed taping reel (K)	7" (178 mm)				

ΝΟΤΕ

1. For Paper/PE/Embossed tape and reel specification/dimensions, please refer to data sheet "Chip resistors packing".

Chip Resistor Surface Mount AH SERIES

FUNCTIONAL DESCRIPTION

OPERATING TEMPERATURE RANGE

Range: -55 °C to +155 °C

Power rating

Each type rated power at 70 °C: AH0603=1/10W; AH0805=1/8W; AH1206=1/4W

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$$V = \sqrt{(P \times R)}$$

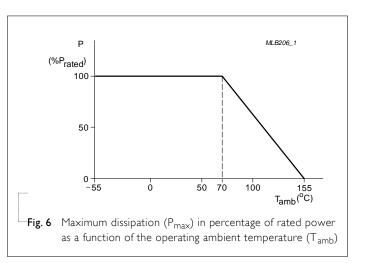
or max. working voltage whichever is less Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

 $R = Resistance value (\Omega)$

Maximum working voltage can be applicable to resistors only if the resistance value is equal to or higher than the critical resistance value.



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TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Life/ Operational Life/ Endurance	MIL-STD-202G-method 108A IEC 60115-1 4.25.1 JIS C 5202-7.10	I,000 hours at 70±5 °C applied RCVVV I.5 hours on, 0.5 hour off, still air required	±(2%+0.05 Ω)
High Temperature Exposure/ Endurance at upper category temperature	MIL-STD-202G-method 108A IEC 60115-1 4.25.3 JIS C 5202-7.11	1,000 hours at maximum operating temperature depending on specification, unpowered No direct impingement of forced air to the parts Tolerances: 155±3 °C	±(1%+0.05 Ω)
Moisture Resistance	MIL-STD-202G-method 106F IEC 60115-1 4.24.2	Each temperature / humidity cycle is defined at 8 hours (method 106F), 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered Parts mounted on test-boards, without condensation on parts Measurement at 24±2 hours after test conclusion	±(2%+0.05 Ω)
Thermal Shock	MIL-STD-202G-method 107G	-55/+125 °C Note: Number of cycles required is 300. Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	±(0.5%+0.05 Ω) for 10 KΩ to 10 MΩ ±(1%+0.05 Ω) for others
Short time overload	MIL-R-55342D-para 4.7.5 IEC60115-1 4.13	2.5 times RCWV or maximum overload voltage whichever is less for 5 sec at room temperature	±(2%+0.05 Ω) No visible damage
Board Flex/ Bending	IEC60115-14.33	Device mounted on PCB test board as described, only 1 board bending required Bending for 0603 & 0805: 3mm 1206 & above: 2mm Holding time: minimum 60 seconds Ohmic value checked during bending	±(1%+0.05 Ω) No visible damage
Biased Humidity	AEC-Q200 Test 7 MIL-STD-202 Method 103	I,000 hours; 85°C / 85% RH I0% of operating power Measurement at 24 ±4 hours after test conclusion	± (5.0%+0.05 Ω)

Chip Resistor Surface Mount AH SERIES 0603/0805/1206 (RoHS Compliant)

Product specification

7 8

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability			
- Wetting	IPC/JEDECJ-STD-002B test B	Electrical Test not required	Well tinned (≥95% covered)
	IEC 60068-2-58	Magnification 50X	No visible damage
		SMD conditions:	
		I st step: method B, aging 4 hours at 155 °C dry heat	
		2 nd step: leadfree solder bath at 245±3 °C	
		Dipping time: 3±0.5 seconds	
- Leaching	IPC/JEDECJ-STD-002B test D IEC 60068-2-58	Leadfree solder, 260 °C, 30 seconds immersion time	No visible damage
- Resistance to	MIL-STD-202G-method 210F	Condition B, no pre-heat of samples	±(1%+0.05 Ω)
Soldering Heat	IEC 60068-2-58	Leadfree solder, 260 °C, 10 seconds immersion time	No visible damage
		Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	
- FOS	ASTM-B-809-95*	Sulfur 750 hours, 105°C, unpowered	±(4.0%+0.05Ω)
	* Modified	····	. ,

YAGEO	Phícomp				Product specification	8
	Chip Resistor Surface Mount	AH	SERIES	0603/0805/1206 (RoHS Compliant)		8
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<u>revision</u>	<u>HISTORY</u>					

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 0	Sep. 10, 2020	-	- First issue of this specification

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